

NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDA)
- **Built-In Biasing Resistors**
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Part Number	R1 (NOM)	R2 (NOM)
DDC124EU	22ΚΩ	22ΚΩ
DDC144EU	47ΚΩ	47ΚΩ
DDC114YU	10ΚΩ	47ΚΩ
DDC123JU	2.2ΚΩ	47ΚΩ
DDC114EU	10ΚΩ	10ΚΩ
DDC143ZU	4.7ΚΩ	47ΚΩ
DDC115EU	100ΚΩ	100ΚΩ

Mechanical Data

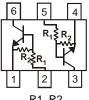
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

Part Number	R1 Only
DDC113TU	1ΚΩ
DDC143TU	4.7ΚΩ
DDC114TU	10ΚΩ

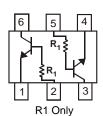
SOT363



Top View



R1, R2



Device Schematic

Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDC124EU-7-F	AEC-Q101	N17	7	8	3,000
DDC124EUQ-7-F	Automotive	N17	7	8	3,000
DDC144EU-7-F	AEC-Q101	N20	7	8	3,000
DDC114YU-7-F	AEC-Q101	N14	7	8	3,000
DDC114YUQ-7-F	Automotive	N14	7	8	3,000
DDC114YUQ-13-F	Automotive	N14	13	8	10,000
DDC123JU-7-F	AEC-Q101	N06	7	8	3,000
DDC114EU-7-F	AEC-Q101	N13	7	8	3,000
DDC114EUQ-7-F	Automotive	N13	7	8	3,000
DDC114EUQ-13-F	Automotive	N13	13	8	10,000
DDC113TU-7-F	AEC-Q101	N01	7	8	3,000
DDC143TU-7-F	AEC-Q101	N07	7	8	3,000
DDC114TU-7-F	AEC-Q101	N12	7	8	3,000
DDC114TUQ-7-F	Automotive	N12	7	8	3,000
DDC143ZU-7-F	AEC-Q101	N03	7	8	3,000
DDC115EU-7-F	AEC-Q101	N02	7	8	3,000

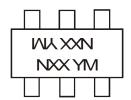
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information

SOT363



NXX = Product Type Marking Code See Page 1 Diagrams YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

Year	2010	2011	2012	2013	3 201	14 20	015	2016	2017	2018	2019	2020
Code	Х	Υ	Z	А	В		С	D	Е	F	G	Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

С	haracteristic	Symbol	Value	Unit	
Supply Voltage		Vcc	50	V	
Input Voltage	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC113TU DDC143TU DDC114TU DDC114TU DDC114ZU DDC115EU	V _{IN}	-10 to +40 -10 to +40 -6 to +40 -5 to +12 -10 to +40 -5V max -5V max -5V max -5 to +30 -10 to +40	V	
Output Current		I _{C(MAX)}	100	mA	

Thermal Characteristics ($@T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6 & 7)	P _D	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ heta JA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 6. Mounted on FR4 PC Board with minimum recommended pad layout. 7. 150mW per element must not be exceeded.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

For R1 only devices: DDC113TU & DDC143TU & DDC114TU

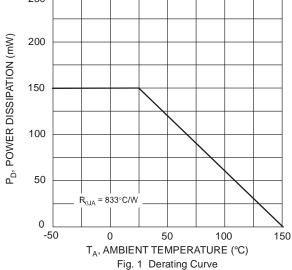
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	50			V	$I_C = 50\mu A$
Collector-Emitter Breakdown Voltage	BV _{CEO}	50		-	V	$I_C = 1mA$
Emitter-Base Breakdown Voltage	BV _{EBO}	5			V	$I_E = 50\mu A$
Collector Cutoff Current	I _{CBO}			0.5	μΑ	$V_{CB} = 50V$
Emitter Cutoff Current	I _{EBO}			0.5	μA	$V_{EB} = 4V$
Collector-Emitter Saturation Voltage	V _{CE(sat)}			0.3	V	I _C /I _B = 2.5mA / 0.25mA DDC143TU I _C /I _B = 1mA / 0.1mA DDC114TU I _C /I _B = 10mA / 1mA DDC113TU
DC Current Transfer Ratio	h _{FE}	100	250	600		$I_C = 1mA$, $V_{CE} = 5V$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	_
Gain-Bandwidth Product	f⊤	_	250	_	MHz	$V_{CE} = 10V$, $I_{E} = -5mA$, $f = 100MHz$

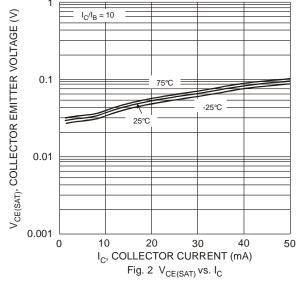
For R1, R2 devices: DDC124EU& DDC144EU& DDC114YU& DDC123JU& DDC114EU&DDC143ZU & DDC115EU

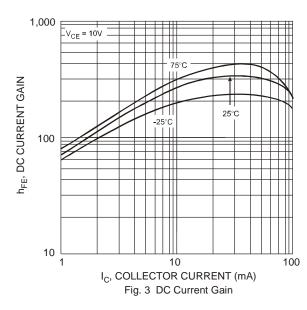
Characterist	ic	Symbol	Min	Тур	Max	Unit	Test Condition
lanut Valtage	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC143ZU DDC115EU	VI(off)	0.5 0.5 0.3 0.5 0.5 0.5	1.1 1.1 — — 1.1 —		V	V _{CC} = 5V, I _O = 100μA
Input Voltage	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC143ZU DDC115EU	VI(on)	_	1.9 1.9 — — 1.9 —	3.0 3.0 1.4 1.1 3.0 1.3 3	V	$\begin{split} &V_{O}=0.3V,\ l_{O}=5\text{mA}\\ &V_{O}=0.3V,\ l_{O}=2\text{mA}\\ &V_{O}=0.3V,\ l_{O}=1\text{mA}\\ &V_{O}=0.3V,\ l_{O}=5\text{mA}\\ &V_{O}=0.3V,\ l_{O}=5\text{mA}\\ &V_{O}=0.3V,\ l_{O}=5\text{mA}\\ &V_{O}=0.3V,\ l_{O}=10\text{Ma}\\ &V_{O}=0.3V,\ l_{O}=5\text{mA}\\ &V_{O}=0.3V,\ l_{O}=1\text{mA}\\ \end{split}$
Output Voltage	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC143ZU DDC115EU	V _{O(on)}	_	0.1	0.3	٧	I _O /I _I = 10mA / 0.5mA I _O /I _I = 10mA / 0.5mA I _O /I _I = 5mA / 0.25mA I _O /I _I = 5mA / 0.25mA I _O /I _I = 10mA / 0.5mA I _O /I _I = 5mA / 0.25mA I _O /I _I = 10mA / 0.5mA
Input Current	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC143ZU DDC115EU	lı	_	_	0.36 0.18 0.88 3.6 0.88 1.8 0.15	mA	V _I = 5V
Output Current		I _{O(off)}	_	_	0.5	μΑ	$V_{CC} = 50V$, $V_I = 0V$
DC Current Gain	DDC124EU DDC144EU DDC114YU DDC114YUQ DDC123JU DDC114EU DDC143ZU DDC115EU	G _l	56 68 68 80 80 30 80 82	_	_	_	$V_0 = 5V$, $I_0 = 5mA$ $V_0 = 5V$, $I_0 = 5mA$ $V_0 = 5V$, $I_0 = 10mA$ $V_0 = 5V$, $I_0 = 5mA$ $V_0 = 5V$, $I_0 = 10mA$ $V_0 = 5V$, $I_0 = 5mA$ $V_0 = 5V$, $I_0 = 10mA$ $V_0 = 5V$, $I_0 = 5mA$
Input Resistor (R ₁) Tolerance		ΔR_1	-30	_	+30	%	
Resistance Ratio Tolerance		$\Delta(R_2/R_1)$	-20		+20	%	
Gain-Bandwidth Product		f _T	_	250	_	MHz	$V_{CE} = 10V, I_{E} = 5mA, f = 100MHz$

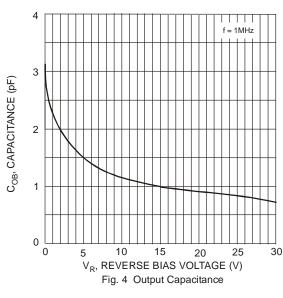


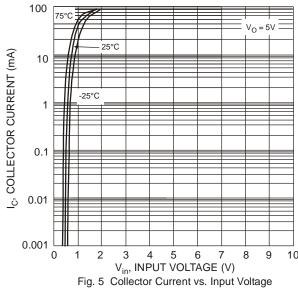
Typical Curves – DDC123JUOne Section (@T_A = +25°C, unless otherwise specified.)

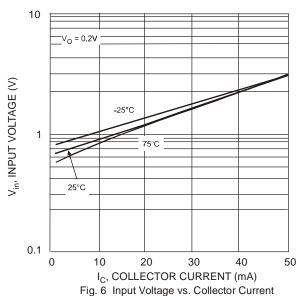






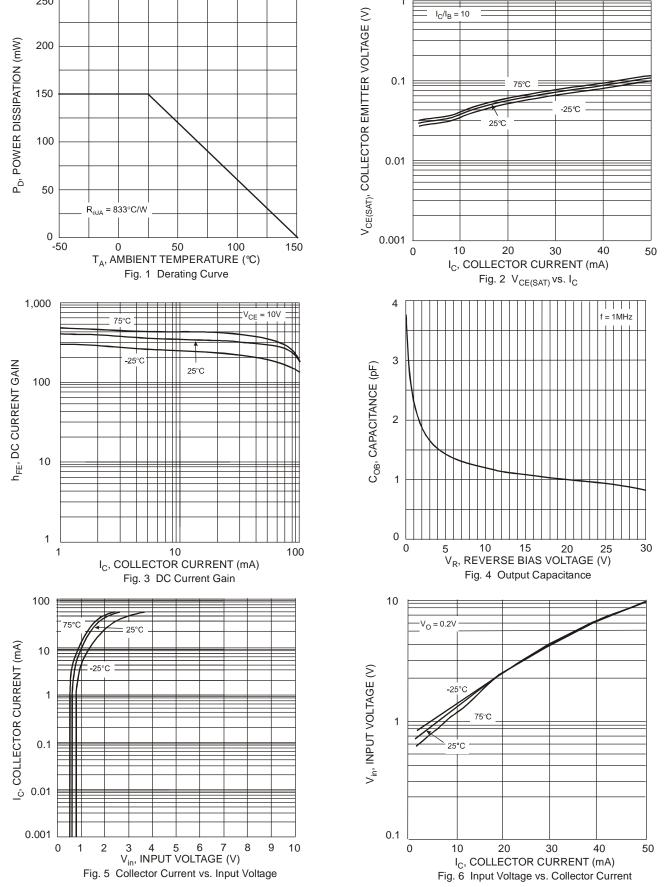








Typical Curves - DDC114YUOne Section (@T_A = +25°C, unless otherwise specified.)





Typical Curves - DDC124EUOne Section (@T_A = +25°C, unless otherwise specified.)

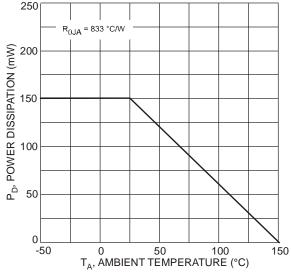


Fig. 1 Power Dissipation vs. Ambient Temeprature

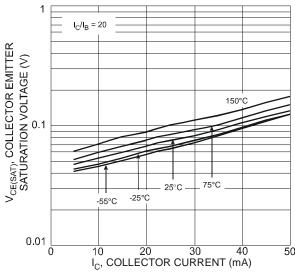


Fig. 3 Collector Emitter Saturation Voltage vs. Collector Current

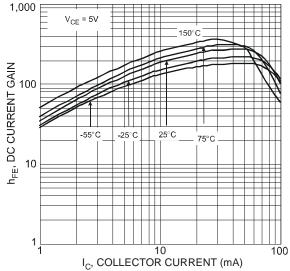


Fig. 2 Typical DC Current Gain vs. Collector Current

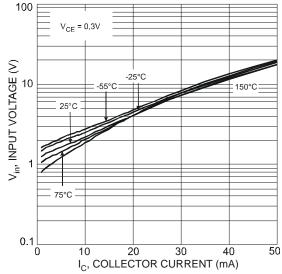
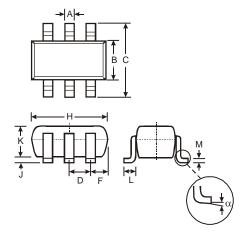


Fig. 4 Input Voltage vs. Collector Current



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

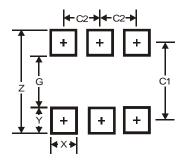


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Dim	Min	Тур						
Α	0.10	0.30	0.25					
В	1.15	1.35	1.30					
С	2.00	2.20	2.10					
D	0.65 Typ							
F	0.40	0.45	0.425					
Н	1.80	1.80 2.20						
J	0	0.10	0.05					
K	0.90	1.00	1.00					
L	0.25	0.40	0.30					
М	0.10	0.10 0.22						
α 0° 8°								
All	All Dimensions in mm							

Suggested Pad Layout

 $Please see AP02001 \ at \ http://www.diodes.com/datasheets/ap02001.pdf \ for \ the \ latest \ version.$

SOT363



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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