

November 2013

FDP15N40

N-Channel UniFETTM MOSFET 400 V, 15 A, 300 m Ω

Features

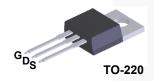
- $R_{DS(on)}$ = 240 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 7.5 A
- Low Gate Charge (Typ. 28 nC)
- Low C_{rss} (Typ. 17 pF)
- · 100% Avalanche Tested
- · Improved dv/dt Capability
- RoHS Compliant

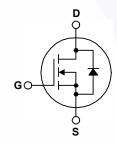
Applications

- · Lighting
- · Uninterruptible Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol			FDP15N40	Unit	
V _{DSS}	Drain to Source Voltage			400	V
V_{GSS}	Gate to Source Voltage			±30	V
	Drain Current	- Continuous (T _C = 25°C)		15	۸
D	Drain Current	- Continuous (T _C = 100°C)		9	Α
I _{DM}	Drain Current	- Pulsed (Note 1)	60	Α
E _{AS}	Single Pulsed Avalanche Energ	gy (Note 2)	731	mJ
I _{AR}	Avalanche Current	(Note 1)	15	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	17	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	15	V/ns
ם	Dower Discinstion	(T _C = 25°C)		170	W
P_{D}	Power Dissipation	- Derate Above 25°C		1.45	W/°C
T _J , T _{STG}	Operating and Storage Temper	ature Range		-55 to +150	°C
TL	Maximum Lead Temperature for	or Soldering, 1/8" from Case for 5 Sec	onds	300	°C

Thermal Characteristics

Symbol	Parameter	FDP15N40	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.7	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	30/00

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDP15N40	FDP15N40	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^{\circ} C$	400	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C	-	0.5	-	V/°C
1	Zero Gate Voltage Drain Current	V _{DS} = 400 V, V _{GS} = 0 V	-	-	1	μА
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 320 \text{ V}, T_{C} = 125^{\circ}\text{C}$	-	-	10	μΑ
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	3.0	-	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$	-	0.24	0.3	Ω
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 7.5 A	-	15.3	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05.V V 0.V		\ -	1310	1750	pF
Coss	Output Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		-	210	280	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1011 12		-\	17	25	pF
Q _{g(tot)}	Total Gate Charge at 10V	V _{DS} = 320 V, I _D = 15 A,		- \	28	36	nC
Q_{gs}	Gate to Source Gate Charge	V _{GS} = 10 V		- \	8	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		(Note 4)	-	12	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	26	62	ns
t _r		$V_{DD} = 200 \text{ V}, I_D = 15 \text{ A},$	-	55	120	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_G = 25 Ω	-	72	154	ns
t _f	Turn-Off Fall Time	(Note 4)	- /	40	90	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		/ -	-	15	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	60	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 15 A	-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 15 A,	-	333	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	-	3.24	/ -	μC

Notes

- 1: Repetitive rating: pulse-width limited by maximum junction temperature.
- 2: L = 6.5 mH, I_{AS} = 15 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C.
- 3: $I_{SD} \leq$ 15 A, di/dt \leq 200 A/ μ s, $V_{DD} \leq$ BV $_{DSS}$, starting T_J = 25°C.
- 4: Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

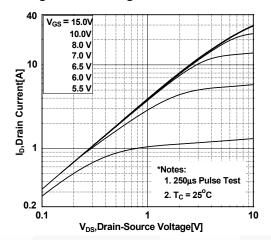


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

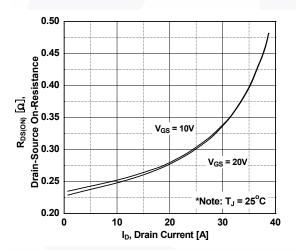


Figure 5. Capacitance Characteristics

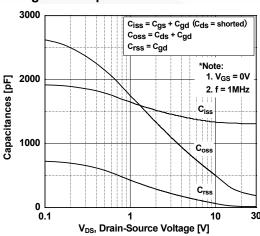


Figure 2. Transfer Characteristics

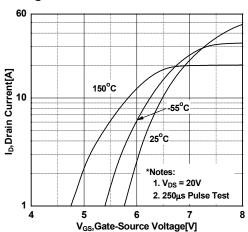


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

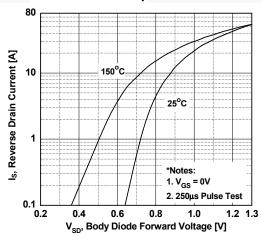
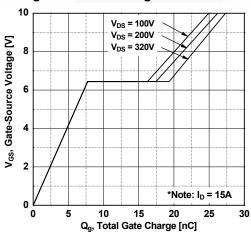


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

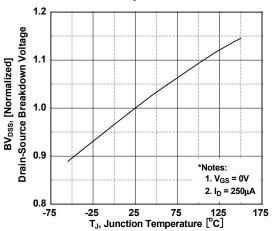


Figure 8. On-Resistance Variation vs. Temperature

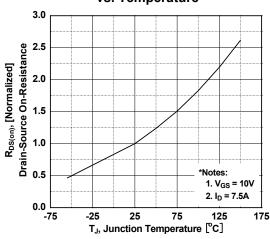


Figure 9. Maximum Safe Operating Area

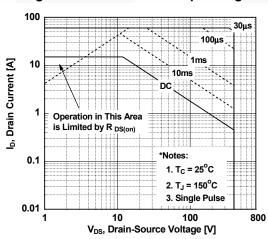


Figure 10. Maximum Safe Operating Area

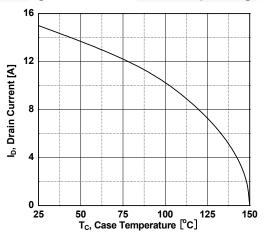
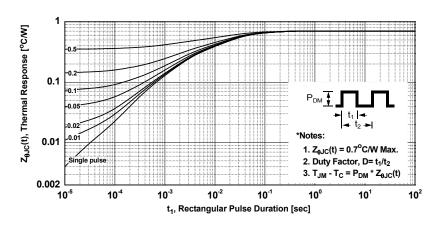


Figure 11. Transient Thermal Response Curve



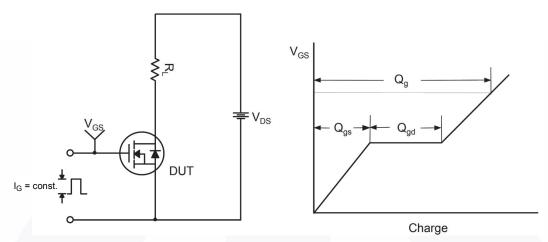


Figure 12. Gate Charge Test Circuit & Waveform



Figure 13. Resistive Switching Test Circuit & Waveforms

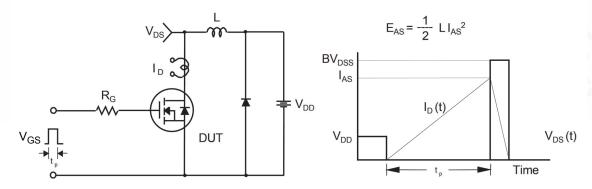


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

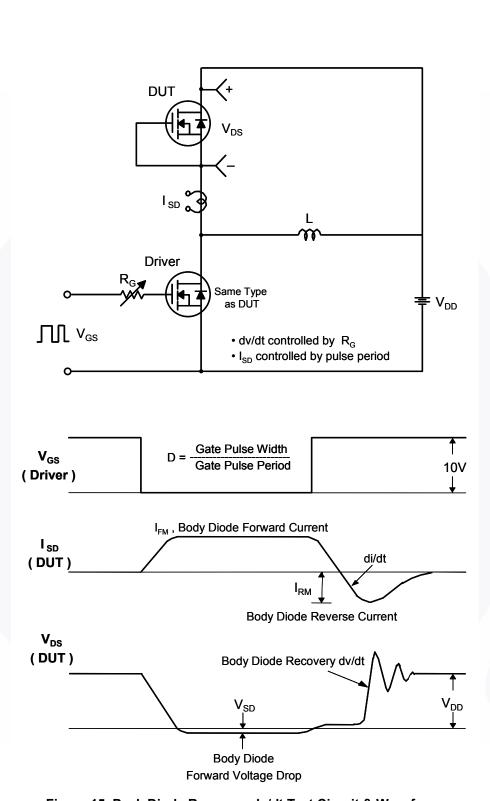


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

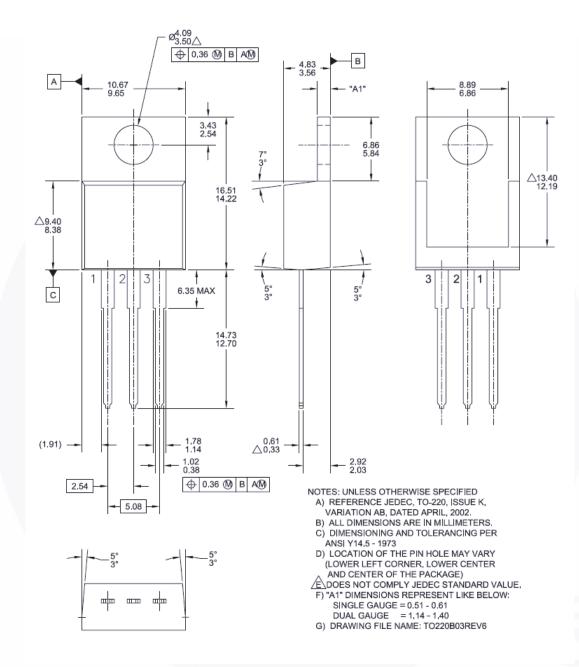


Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

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