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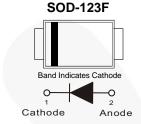


August 2015

# SS13FL / SS14FL Surface Mount Schottky Barrier Rectifier

#### **Features**

- Ultra Thin Profile Maximum Height of 1.08 mm
- UL Flammability 94V-0 Classification
- MSL 1
- · RoHS Compliant / Green Mold Compound
- Industrial Device Qualified per AEC-Q101 Standards.
  - \* see authorized use policy



## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
SS13FL	G3	SOD-123F	Tape and Reel
SS14FL	G4	SOD-123F	Tape and Reel

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Va	Unit	
Symbol	Farameter	SS13FL	SS14FL	Unit
V <sub>RRM</sub>	Peak Reverse Voltage	30	40	V
V <sub>R</sub>	Reverse Voltage	30	40	V
I <sub>F(AV)</sub>	Average Rectified Current at $T_A = 75^{\circ}C$ 1.0		.0	Α
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current at t = 8.3 ms	40		Α
TJ	Operating Junction Temperature Range -55 to +125		+125	°C
T <sub>STG</sub>	T <sub>STG</sub> Storage Temperature Range -55			°C

## Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
ΨJL	Typical Thermal Characteristics, Junction-to-Lead <sup>(2)</sup>	25	°C/W
$R_{\theta JA}$	Typical Thermal Resistance, Junction-to-Ambient	140	°C/W

#### Note:

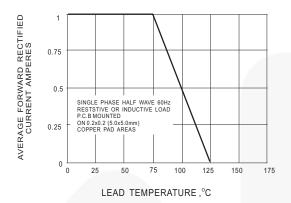
- 1. Per JESD51-3 recommended thermal test board. Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm.
- 2. Thermocouple soldered at cathode lead.

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Condition	s	Min.	Тур.	Max.	Unit
BV <sub>R</sub>	Reverse Breakdown Voltage	I <sub>R</sub> = 500 μA	SS13FL	30			- V
ΒVR			SS14FL	40			
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 1.0 A				0.55	V
I <sub>R</sub>	Reverse Leakage Current	$V_R = V_{RRM}$				30	μΑ
т	Reverse Recovery Time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A	SS13FL		5.875		ns
$T_{rr}$			SS14FL		5.695		
CJ	Junction Capacitance	V <sub>R</sub> = 0			60		pF

## **Typical Performance Characteristics**



**Figure 1. Forward Current Derating Curve** 

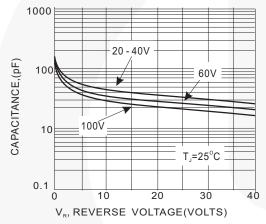


Figure 3. Typical Junction Characteristic

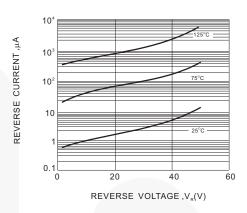
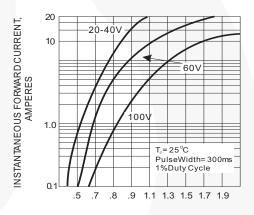


Figure 2. Typical Reverse Characteristic



INSTANTANEOUS FORWARD VOLTAGE, VOLTS

Figure 4. Typical Instantaneous Forward Characteristics

# **Physical Dimensions** 1.95 1.50 2.86 3.90 1.80 3.00 3.30 2.50 (0.35) 1.15 0.50 LAND PATTERN RECOMMENDATION 4°-10° 0.20 1.08 0.80 0.05 +/-0.10 A NOTES: A. THIS PACKAGE DOES NOT CONFORM TO ANY STANDARDS. B. ALL DIMENSIONS ARE IN MILLIMETERS. C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS. D. DRAWING FILE NAME: MA02BREV5 FAIRCHILD , Figure 5. 2-LEAD, SOD123F, NON-JEDEC, FLAT TERMINAL





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Definition of Terms					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.			
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