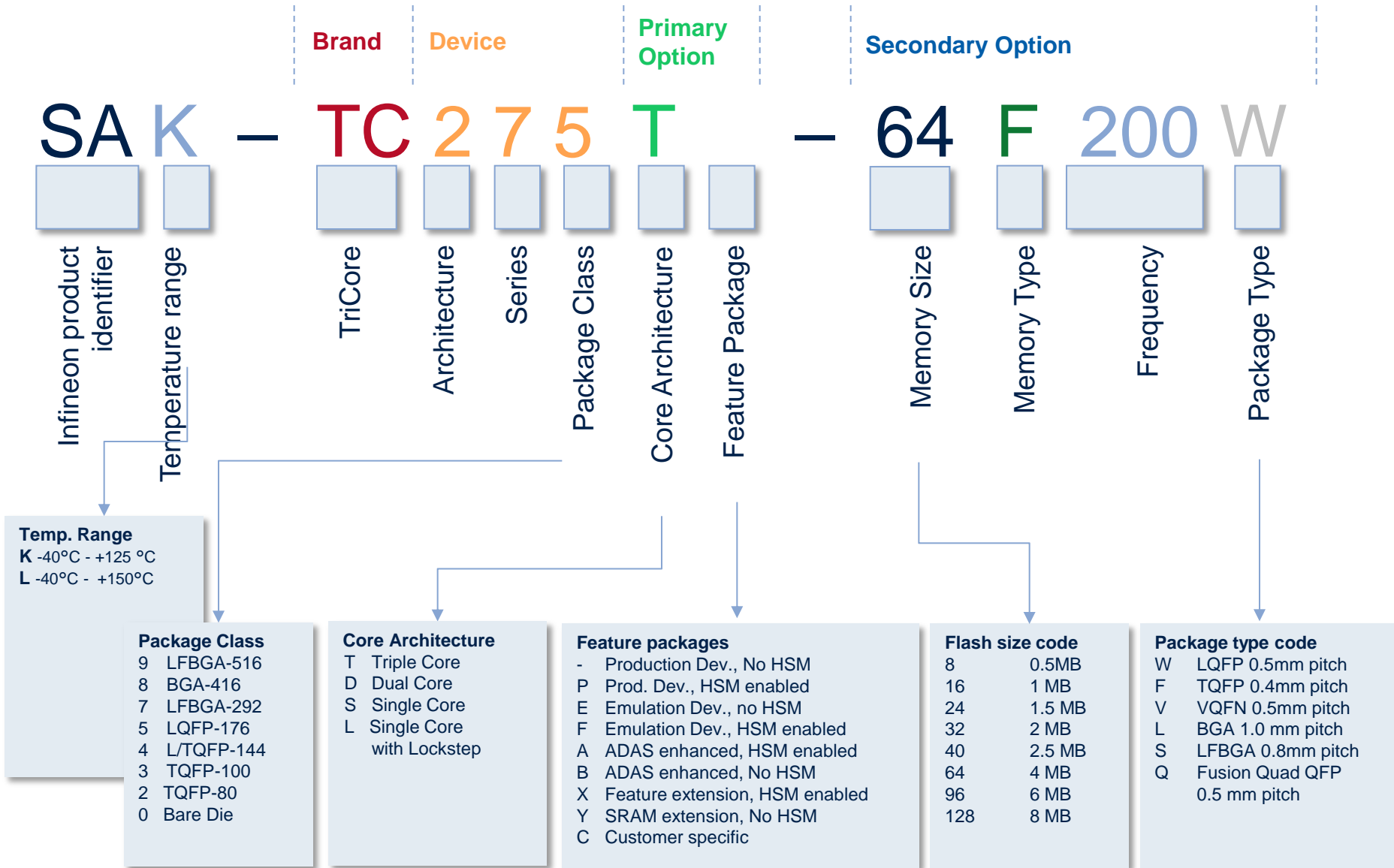


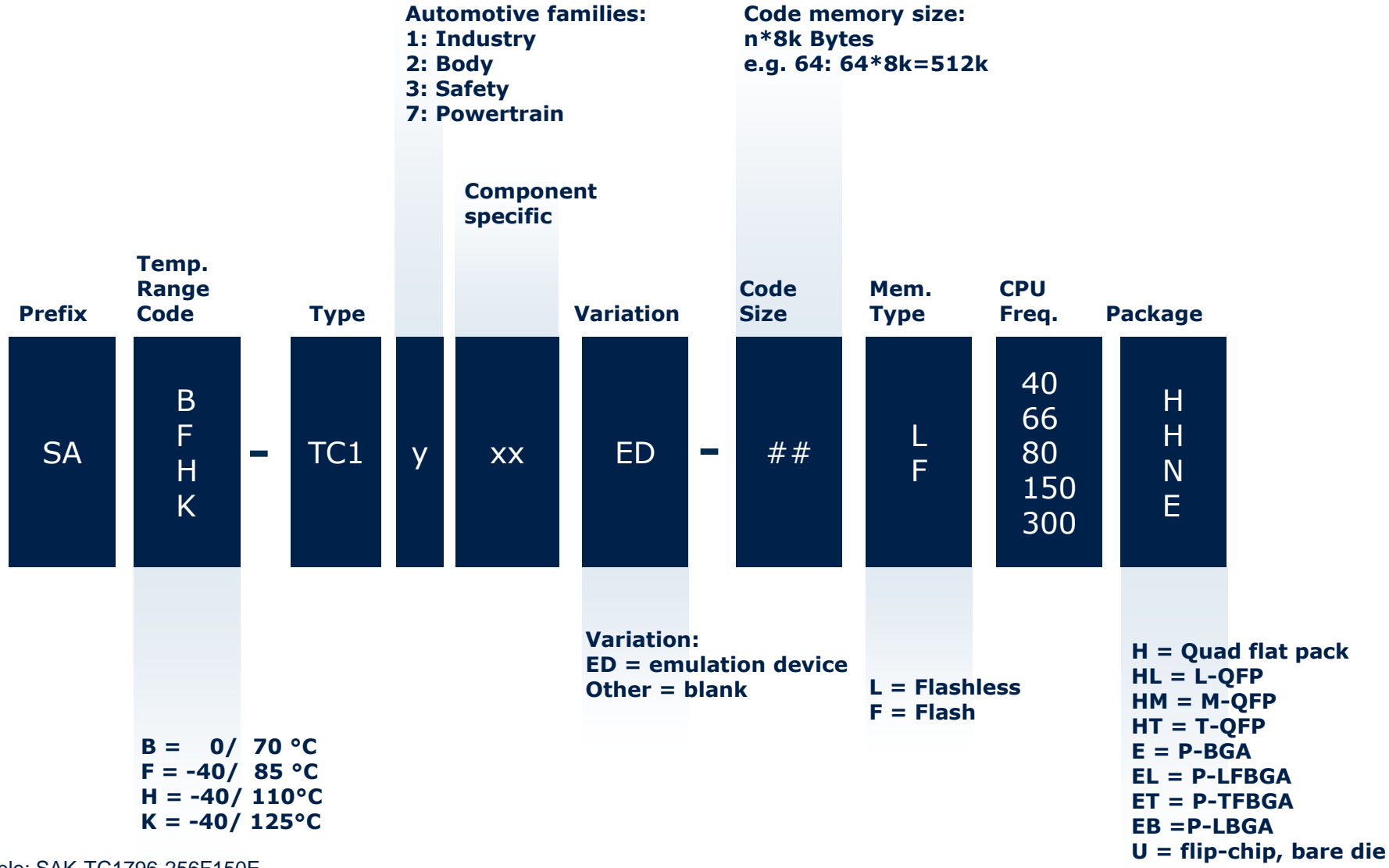
Naming Convention for **AURIX™** Family



Naming Convention for **TriCore®** Family



Example: SAK-TC1796-256F150E



Example: SAK-TC1796-256F150E

Naming Convention for **XMC4000** Family



Example: XMC4500–F144K1024 AC

Family	Core	Feature			Package	Pins	Temp.	Flash [kB]	Step
XMC	4	5	0	0	E F Q	144	K X F	1024	AA AB AC
		4		2		100		768	
		2		4		64		512	
		1		8		48		256	
								128	
							64		

Silicon Version
 AA = 01
 AB = 02
 AC = 03

K = -40° to 125°C
 X = -40° to 105°C
 F = -40° to 85°C

E = LFBGA | F = LQFP | Q = VQFN

0 = Full Featured < > 8 = Feature Subset

1 = Baseline < > 5 = High-End

ARM® Cortex™-M4 with built in DSP, SPFPU, DMA and MPU

XMC := Industrial Microcontroller Family based on ARM® Cortex™-M Processors

Naming Convention for **XMC1000** Family



Example: XMC1302-T038X0200 AA

Family	Core	Feature			Package	Pins	Temp.	Flash [kB]	Step
XMC	1	3	0	2	Q	40	X	200	AA
		2		1	T	38		128	
		1		0		28	F	64	
						24		32	
						16		16	
								8	

Silicon Version
AA = 01

X = -40° to 105°C
F = -40° to 85°C

Q = VQFN | T = TSSOP

2 = Full Featured < > 0 = Feature Subset

1 = Baseline < > 3 = High-End

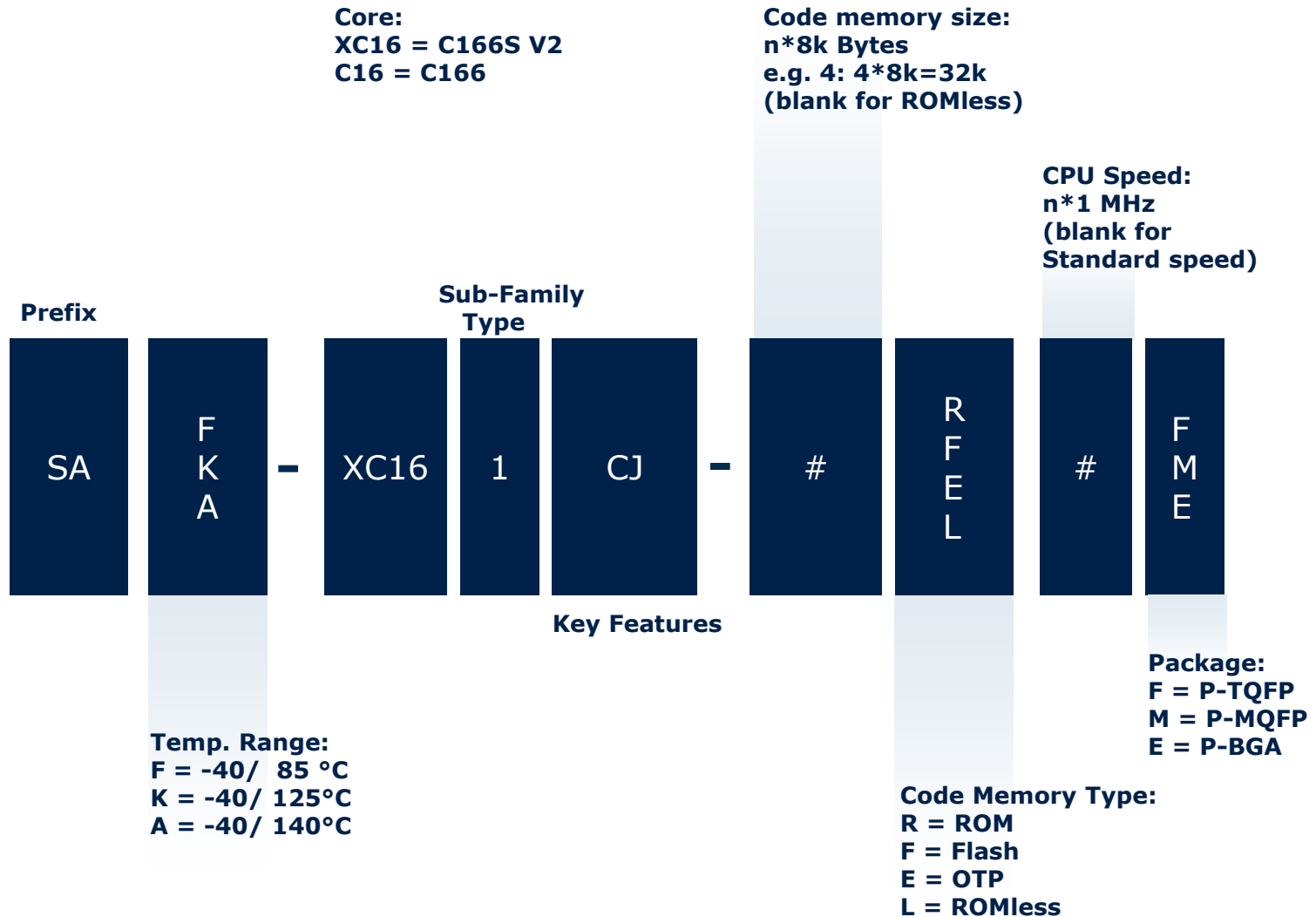
ARM® Cortex™-M0

XMC := Industrial Microcontroller Family based on ARM® Cortex™-M Processors

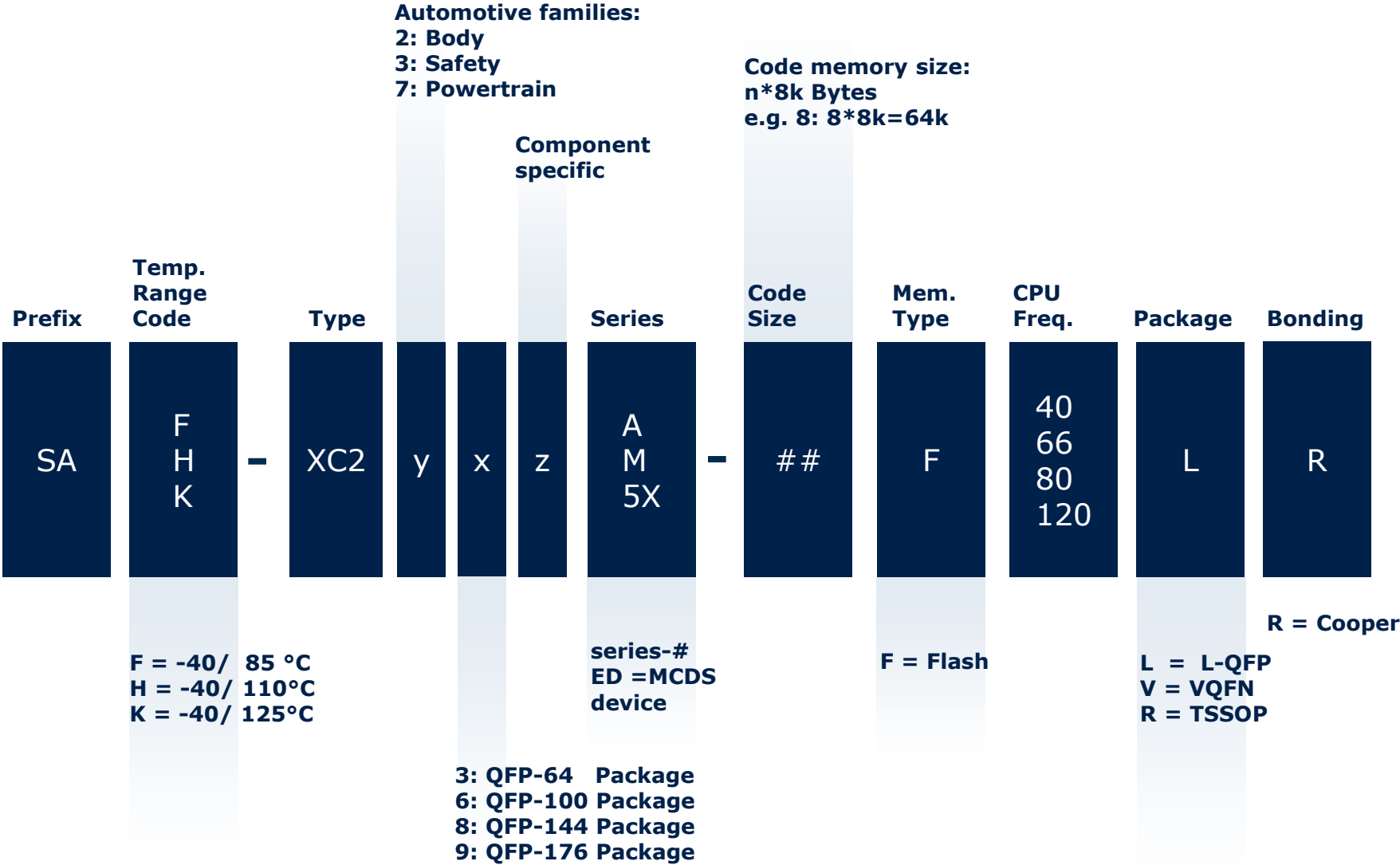
Naming Convention for **16-bit** Family



Example: SAF-XC161CJ-14F20F



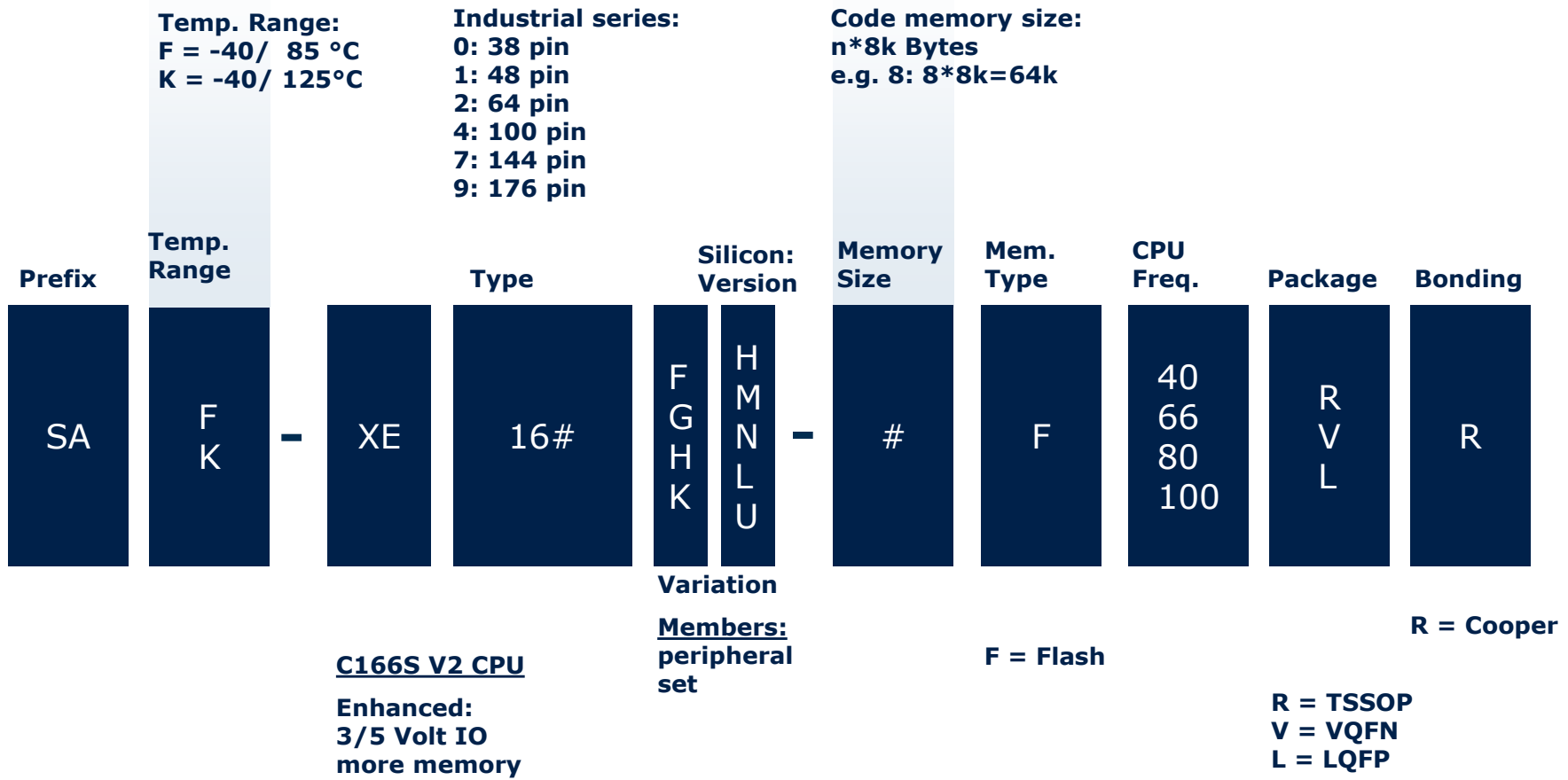
Naming Convention for XC2000 Family



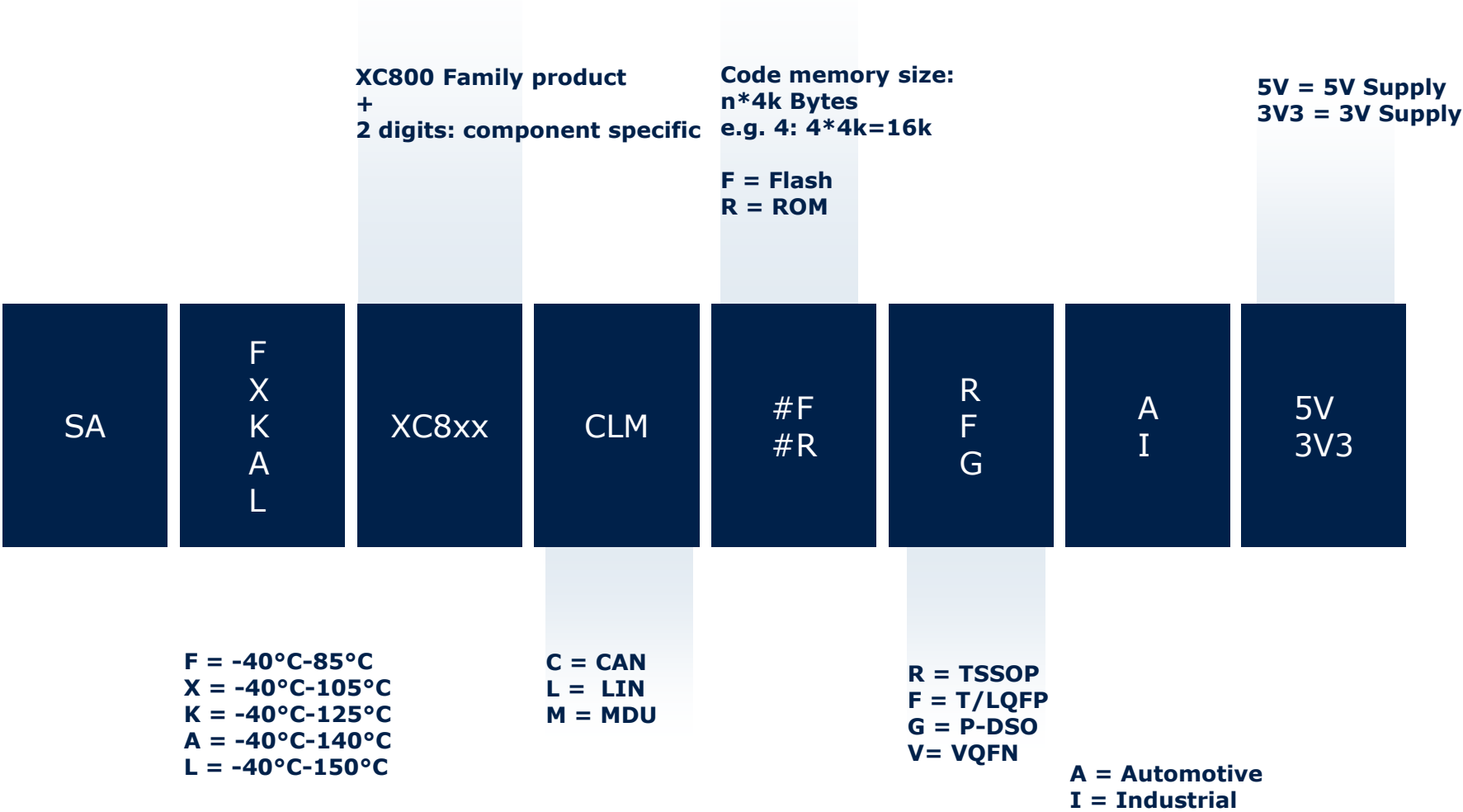
Naming Convention for XE166 Family



Example: SAF-XE167F-96F80L



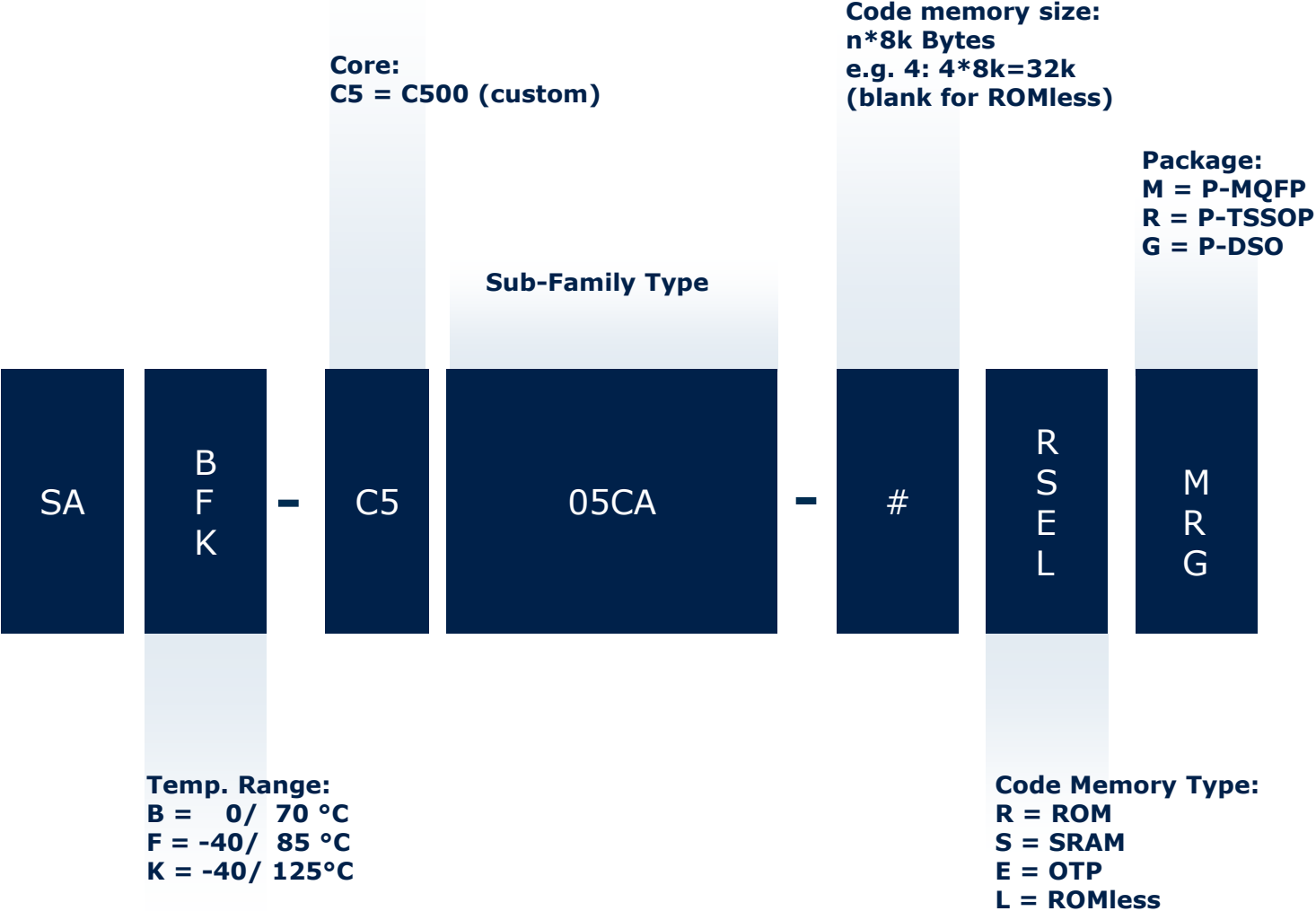
Naming Convention for **XC800** Family



Naming Convention for C500 Family



Example: SAB-C505CA-4EM



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[FX164CM16F40FBAXT](#) [SAF-XC161CJ-16F40F BB](#) [SAF-XC164CS-32F40F BB-A](#) [SAF-XC167CI-32F40F BB-A](#) [SAF-](#)
[XC888C-8FFI 3V3 AC](#) [FX164CS32F40FBAXT](#) [FX167CI32F40FBAXT](#)