

Addendum to MC9S08DV60 Series Data Sheet

Clarifications for MC9S08DV60A/48A/32A/16A Devices

This addendum document describes clarifications to the *MC9S08DV60 Series Data Sheet* (document number: MC9S08DV60). For convenience, the addenda items are grouped by revision. Please check our website at <http://www.freescale.com> for the latest updates.

The *MC9S08DV60 Series Data Sheet* also applies to the MC9S08DV60A series of devices.

For users migrating from devices with the 3M05C mask to devices with the M74K mask, please read the application note *AN3776 MC9S08DZ60 Migrating from the 3M05C to M74K mask set*. This application note applies to the DZ, DV, DN, DE, and EN series of devices.

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1 Clarifications for Revision 4

Table 1. MC9S08DV60 Rev 4 Clarification

Location	Description
Section C.1	<p>Updated ordering information. Added " For 'Fully Qualified' part numbers beginning with 'MC', A = 1M74K mask set".</p> <p> Status (MC = Fully Qualified) (S = Auto Qualified) </p> <p> Memory (9 = Flash-based) </p> <p> Core </p> <p> Family </p> <p> Approximate Flash size in KB </p> <p> Temperature range (C = -40°C to 85°C) (V = -40°C to 105°C) (M = -40°C to 125°C) </p> <p> Mask Set Identifier For "Auto Qualified" part numbers beginning with "S" F1 = 1M74K mask set For "Fully Qualified" part numbers beginning with "MC" A = 1M74K mask set </p> <p> Package designator (see Table C-2) </p>

2 Revision History

Table 2 provides a revision history for this document.

Table 2. MC9S08DV60AD Revision History

Rev. Number	Substantive Changes	Date of Release
1	Initial release	4/27/2009

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