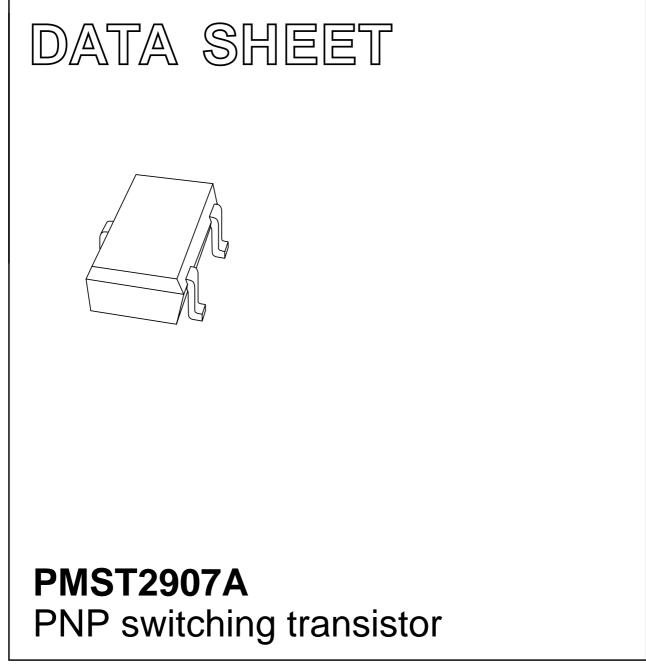
# DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1999 Apr 22 2001 Nov 19



### FEATURES

- Low current (max. 600 mA)
- Low voltage (max. 60 V).

### APPLICATIONS

- Medium power switching
- General purpose amplification.

### DESCRIPTION

PNP switching transistor in an SC-70; SOT323 plastic package. NPN complement: PMST2222A.

#### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>		
PMST2907A	*2F		

### Note

1. \* = - : Made in Hong Kong.

\* = t : Made in Malaysia.

### DIN

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	

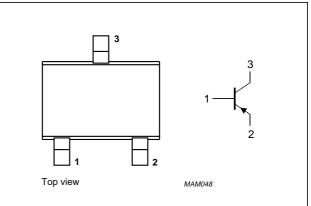


Fig.1 Simplified outline (SC-70; SOT323) and symbol.

### LIMITING VALUES In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-60	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current (DC)		-	-600	mA
I <sub>CM</sub>	peak collector current		-	-800	mA
I <sub>BM</sub>	peak base current		-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	-	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	625	K/W

### Note

1. Transistor mounted on an FR4 printed-circuit board.

### CHARACTERISTICS

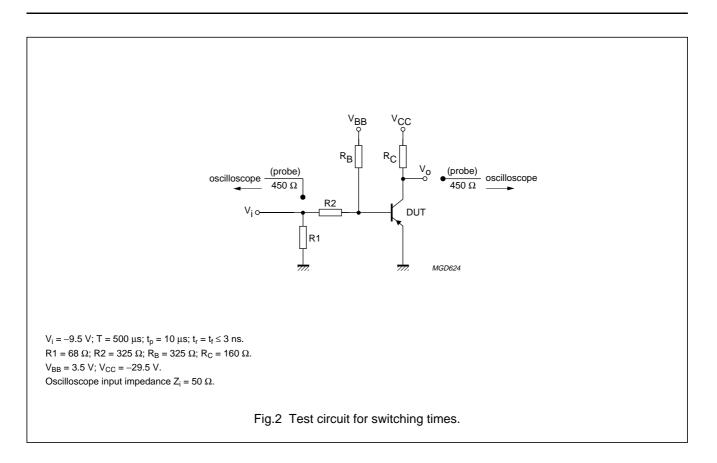
 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	
I <sub>CBO</sub>	collector cut-off current	$I_{E} = 0; V_{CB} = -50 V$	-	-10	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = -50 V; T <sub>j</sub> = 150 °C	-	-10	μA
I <sub>EBO</sub>	emitter cut-off current	$I_{\rm C} = 0; V_{\rm EB} = -3 \text{ V}$	-	-50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -10 V			
		$I_{\rm C} = -0.1  {\rm mA}$	75	-	
		$I_{\rm C} = -1  \rm{mA}$	100	-	
		$I_{\rm C} = -10 \text{ mA}; \text{ note } 1$	100	-	
		I <sub>C</sub> = -150 mA; note 1	100	300	
		I <sub>C</sub> = -500 mA; note 1	50	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C} = -150 \text{ mA}; I_{B} = -15 \text{ mA}; \text{ note } 1$	-	-400	mV
		$I_{C} = -500 \text{ mA}; I_{B} = -50 \text{ mA}; \text{ note } 1$	-	-1.6	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C} = -150 \text{ mA}; I_{B} = -15 \text{ mA}; \text{ note } 1$	-	-1.3	V
		$I_{C} = -500 \text{ mA}; I_{B} = -50 \text{ mA}; \text{ note } 1$	-	-2.6	V
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0; V_{CB} = -10 V; f = 1 MHz$	-	8	pF
C <sub>e</sub>	emitter capacitance	$I_{C} = i_{c} = 0; V_{EB} = -2 V; f = 1 MHz$	-	30	pF
f <sub>T</sub>	transition frequency	$I_{C} = -50 \text{ mA}; V_{CE} = -20 \text{ V};$ f = 100 MHz; note 1	200	-	MHz
Switching t	imes (between 10% and 90% leve	<b>Is);</b> (see Fig.2)	•		•
t <sub>on</sub>	turn-on time	$I_{Con} = -150 \text{ mA}; I_{Bon} = -15 \text{ mA};$ $I_{Boff} = 15 \text{ mA}$	-	45	ns
t <sub>d</sub>	delay time		-	15	ns
t <sub>r</sub>	rise time		-	35	ns
t <sub>off</sub>	turn-off time	]	_	300	ns
t <sub>s</sub>	storage time	1	-	250	ns
t <sub>f</sub>	fall time	1	_	50	ns

### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

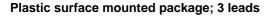
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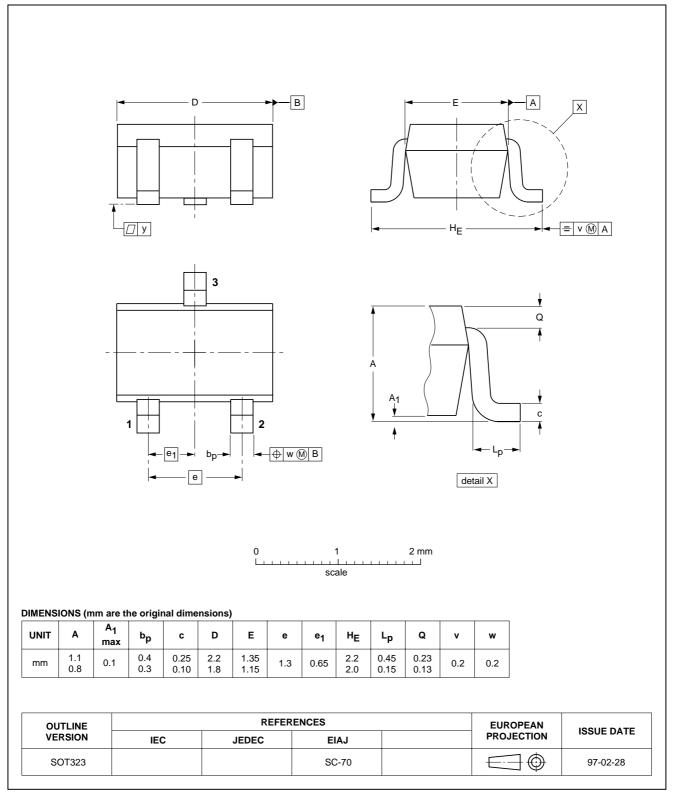


**PMST2907A** 

## PNP switching transistor

### PACKAGE OUTLINE





PMST2907A

### DATA SHEET STATUS

DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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NOTES

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