DMA204A0

Silicon PNP epitaxial planar type

For low frequency amplification

■ Features

- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Halogen-free / RoHS compliant
  (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

■ Marking Symbol: C0

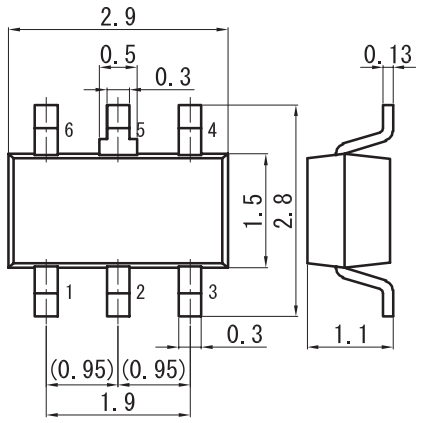
■ Basic Part Number

Dual DSA2401 (Individual)

■ Packaging

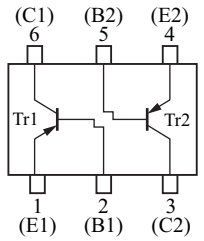
DMA204A00R Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

Unit: mm



1: Emitter (Tr1) 4: Emitter (Tr2)
2: Base (Tr1) 5: Base (Tr2)
3: Collector (Tr2) 6: Collector (Tr1)

Panasonic	Mini6-G4-B
JEITA	SC-74
Code	SOT-457



■ Absolute Maximum Ratings $T_a = 25°C$

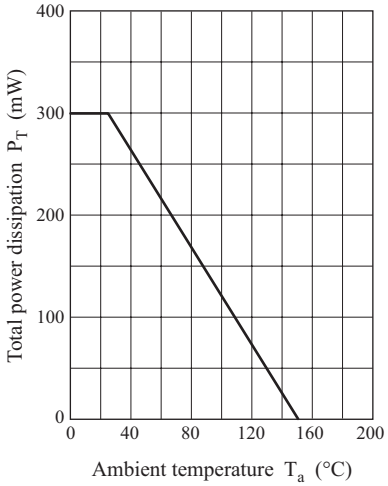
Parameter		Symbol	Rating	Unit
Tr1 Tr2	Collector-base voltage (Emitter open)	V_{CBO}	–15	V
	Collector-emitter voltage (Base open)	V_{CEO}	–10	V
	Emitter-base voltage (Collector open)	V_{EBO}	–7	V
	Collector current	I_C	– 0.5	A
	Peak collector current	I_{CP}	–1	A
Overall	Total power dissipation	P_T	300	mW
	Junction temperature	T_j	150	°C
	Operating ambient temperature	T_{opr}	–40 to +85	°C
	Storage temperature	T_{stg}	–55 to +150	°C

■ Electrical Characteristics $T_a = 25°C \pm 3°C$

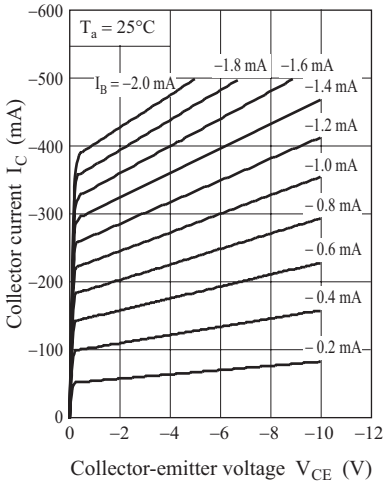
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = –10$ μA, $I_E = 0$	–15			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = –1$ mA, $I_B = 0$	–10			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = –10$ μA, $I_C = 0$	–7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = –10$ V, $I_E = 0$			–100	nA
Forward current transfer ratio *1	h_{FE1}	$V_{CE} = –2$ V, $I_C = – 0.5$ A	130		350	—
	h_{FE2}	$V_{CE} = –2$ V, $I_C = –1$ A	60			
Collector-emitter saturation voltage *1	$V_{CE(sat)}$	$I_C = – 0.4$ A, $I_B = –8$ mA		– 0.15	– 0.30	V
Base-emitter saturation voltage *1	$V_{BE(sat)}$	$I_C = – 0.4$ A, $I_B = –8$ mA		– 0.8	–1.2	V
Transition frequency	f_T	$V_{CE} = –10$ V, $I_C = –50$ mA		250		MHz
Collector output capacitance (Common base, input open circuited)	C_{ob}	$V_{CB} = –10$ V, $I_E = 0$, f = 1 MHz		18		pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.
2. *1: Pulse measurement

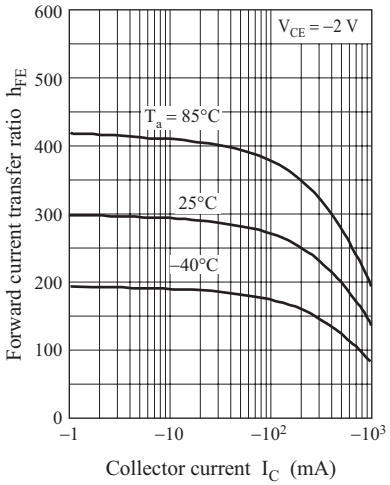
P_T — T_a



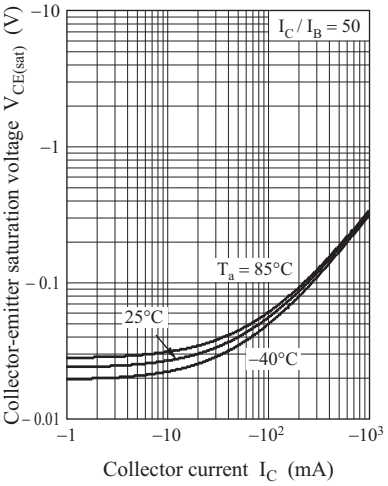
I_C — V_{CE}



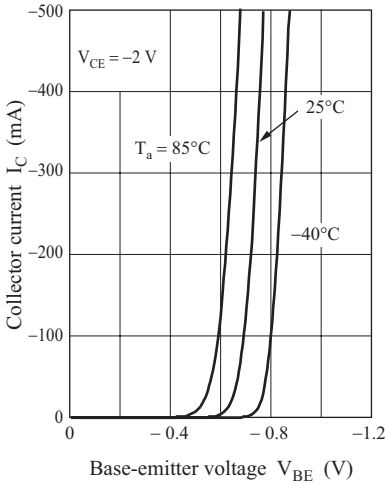
h_{FE} — I_C



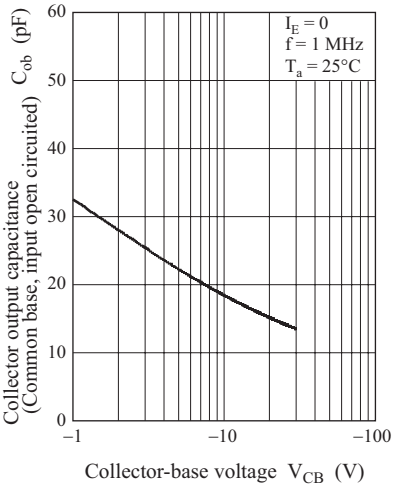
$V_{CE(sat)}$ — I_C



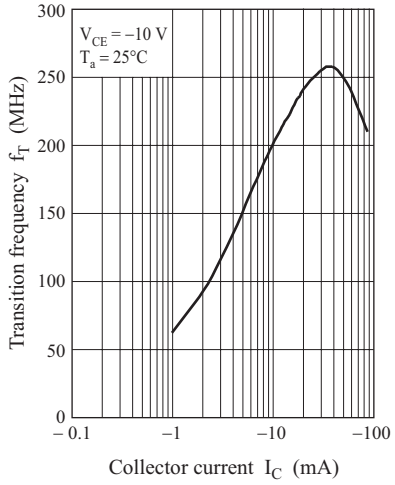
I_C — V_{BE}



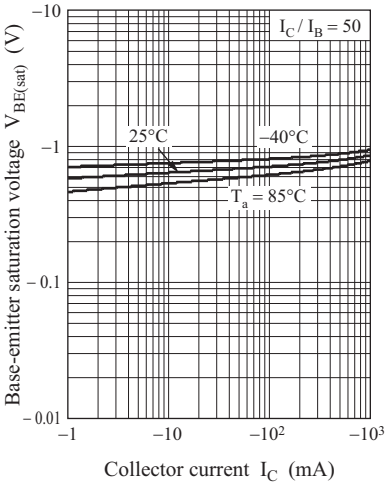
C_{ob} — V_{CB}



f_T — I_C

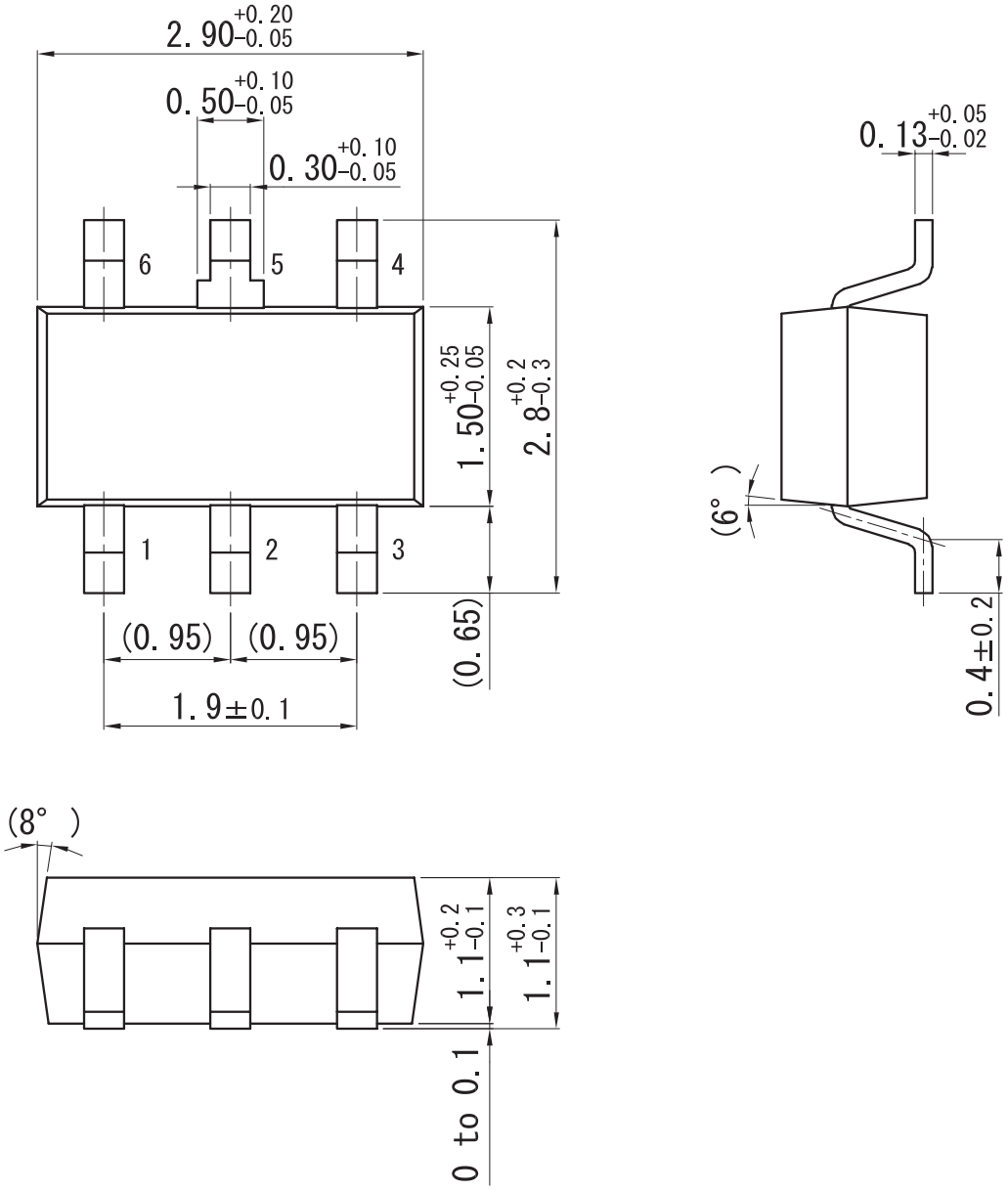


$V_{BE(sat)}$ — I_C

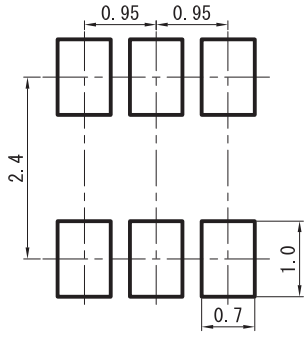


Mini6-G4-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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