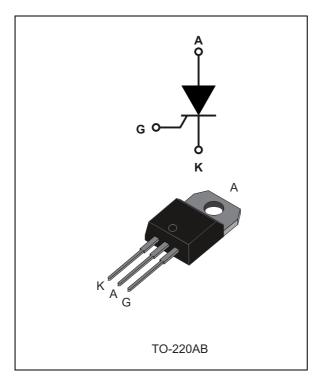


TN2015H-6T

High temperature 20 A SCRs

Datasheet – production data



Features

- High junction temperature: T_i = 150 °C
- High noise immunity dV/dt = 750 V/µs up to 150 °C
- Gate triggering current I_{GT} = 15 mA
- Blocking voltage V_{DRM}/V_{RRM} = 600 V
- High turn on current rise dl/dt: 100 A/µs
- ECOPACK[®]2 compliant component

Applications

- Voltage regulator circuits for motorbikes
- Inrush current limiting circuits
- Motor control circuits and starters
- Light dimmers
- Solid state relays

February 2015

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This is information on a product in full production.

Thanks to a junction temperature T_j up to 150 °C and a pop isolated TO 220 package, the

Description

and a non-isolated TO-220 package, the TN2015H-6T offers high thermal performance up to 20 A rms.

The trade-off between the device's noise immunity (dV/dt = 750 V/ μ s), its gate triggering current (I_{GT} = 15 mA) and its turn-on current rise (dI/dt = 100 A/ μ s) allows the design of robust and compact control circuits for voltage regulators in motorbikes and industrial drives, overvoltage crowbar protection, motor control circuits in power tools and kitchen appliances, and inrush current limiting circuits.

Table 1. Device summary

Order code	Package	V _{DRM} /V _{RRM}	I _{GT}
TN2015H-6T	TO-220AB	600 V	15 mA

1 Characteristics

Symbol	Parameter			Value	Unit
I _{T(RMS)}	On-state rms current (180° conduction angle) $T_c =$			20	А
			T _c = 132 °C	12.7	
I _{T(AV)}	Average on-state current (180° conduction angle)		T _c = 137 °C	10	А
		T _c = 140 °C	8		
1.	Non repetitive surge peak on-state curre	nt	t = 8.3 ms	197	А
ITSM	(T _j initial = 25 °C)		t = 10 ms	180	~
l²t	$I^{2}t$ value for fusing (T _j initial = 25 °C) $t_{p} = 10 \text{ ms}$			162	A ² s
dl/dt	$ \begin{array}{l} \mbox{Critical rate of rise of on-state current} \\ I_G = 2 \ x \ I_{GT}, \ t_r \leq 100 \ ns, \ T_j = 25 \ ^\circ C \end{array} \end{array} \ F = 60 \ Hz \ \label{eq:F} $			100	A/µs
V _{DRM} , V _{RRM}	Repetitive peak off-state voltage	Repetitive peak off-state voltage			V
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	А
P _{G(AV)}	Average gate power dissipation $T_j = 150 \text{ °C}$			1	W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 150	°C
ΤL	Maximum lead temperature for soldering during 10 s			260	°C

Symbol	Test conditions			Value	Unit
L	$V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm I} = 33 \Omega$		Тур.	6	mA
I _{GT}	$v_{\rm D} = 12 v, n_{\rm L} = 33 22$		Max.	15	IIIA
V _{GT}	V_D = 12 V, R _L = 33 Ω		Max.	1.3	V
V _{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	T _j = 150 °C	Min.	0.2	V
Ι _Η	I _T = 500 mA, gate open			50	mA
١L	$I_G = 1.2 \times I_{GT}$		Max.	60	mA
dV/dt	V_D = 402 V, gate open T_j = 150 °C		Min.	750	V/µs
t _{gt}	$I_{T} = 40 \text{ A}, V_{D} = 600 \text{ V}, I_{G} = 100 \text{ mA}, \\ (\text{d}I_{G}/\text{d}t)\text{max} = 0.2 \text{ A}/\mu\text{s}$		Тур	1.9	μs
t _q	$ \begin{array}{c c} V_D = 402 \ V, \ V_R = 25 \ V, \ I_T = 20 \ A, \\ (dI_G/dt)max = 30A/\mu s, \ dV_D/dt = 50 \ V/\mu s \end{array} \ T_j = 150 \ ^\circ C \ \end{array} $		Тур	70	μs



Symbol	Test conditions			Value	Unit
V _{TM}	I _{TM} = 40 A, t _p = 380 μs	T _j = 25 °C	Max.	1.6	V
V _{t0}	Threshold voltage	T _j = 150 °C	Max.	0.82	V
R _d	Dynamic resistance	T _j = 150 °C	Max.	17.5	mΩ
I _{DRM,}		T _j = 25 °C	Max.	5	μA
I _{RRM}			widx.	2	mA

Table 4. Static characteristics

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	1.0	°C/W
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus average on-state current

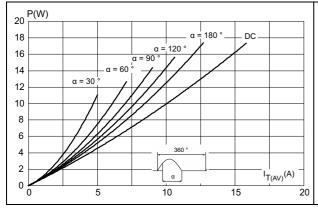


Figure 2. Average and DC on-state current versus case temperature

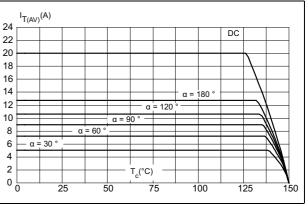
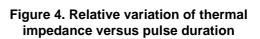
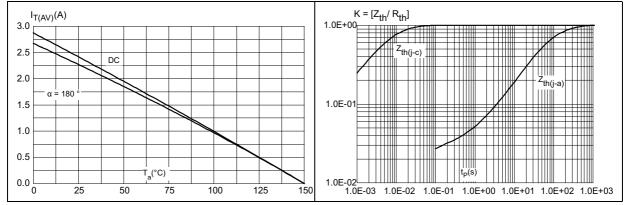


Figure 3. Average and DC on-state current versus ambient temperature







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Figure 5. Relative variation of gate triggering current and gate voltage versus junction temperature (typical values)

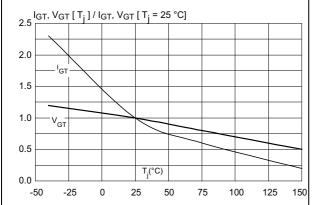
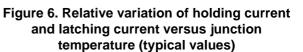
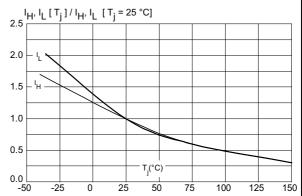
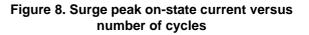


Figure 7. Relative variation of static dV/dt immunity versus junction temperature (typical values)







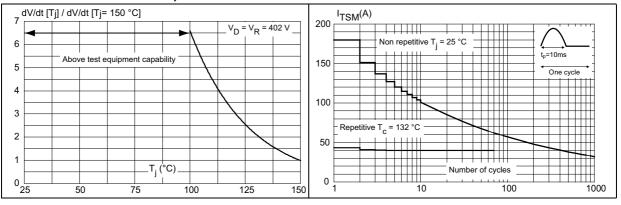
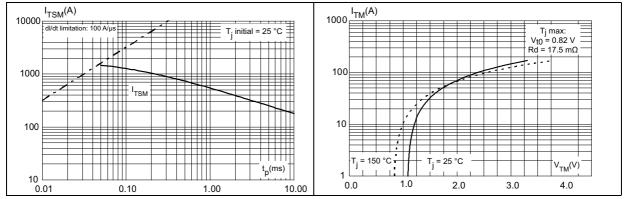


Figure 9. Non-repetitive surge peak on-state current for a sinusoidal pulse (tp < 10 ms)

Figure 10. On-state characteristics (maximum values)





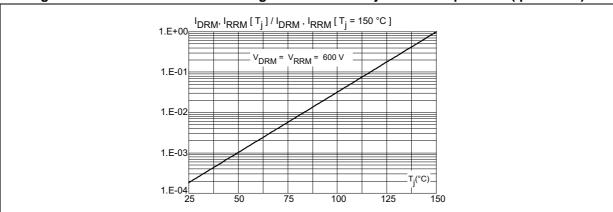


Figure 11. Relative variation of leakage current versus junction temperature (tp < 10 ms)



2 Package information

- Epoxy meets UL94, V0
- Lead-free package
- Halogen free molding compound
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

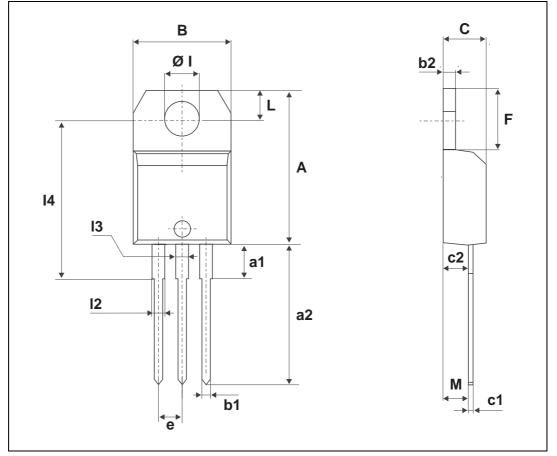


Figure 12. TO-220AB dimension definitions



Dimensions							
Ref.		Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	15.20		15.90	0.598		0.625	
a1		3.75			0.147		
a2	13.00		14.00	0.511		0.551	
В	10.00		10.40	0.393		0.409	
b1	0.61		0.88	0.024		0.034	
b2	1.23		1.32	0.048		0.051	
С	4.40		4.60	0.173		0.181	
c1	0.49		0.70	0.019		0.027	
c2	2.40		2.72	0.094		0.107	
е	2.40		2.70	0.094		0.106	
F	6.20		6.60	0.244		0.259	
ØI	3.75		3.85	0.147		0.151	
14	15.80	16.40	16.80	0.622	0.646	0.661	
L	2.65		2.95	0.104		0.116	
12	1.14		1.70	0.044		0.066	
13	1.14		1.70	0.044		0.066	
М		2.60			0.102		

Table 6. TO-220AB dimension values



3 Ordering information

20 15 H - 6 T
-

Figure 13. Ordering information scheme

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN2015H-6T	TN2015H6	TO-220AB	2.3 g	50	Tube

4 Revision history

Date	Revision	Changes
25-Feb-2015	1	Initial release.



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