

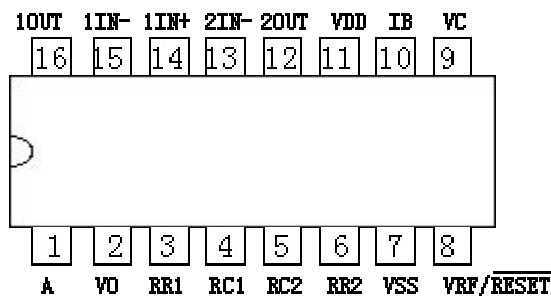
BISS0001 hotly releases electricity infrared sensor signal processing integrated circuit

BISS0001 is section has compares the high performance sensor the signal processing integrated circuit. Outside it matches to the heat releases the electricity infrared sensor and is few meets the primary device constitution passive form the heat to release the electricity infrared switch. It can automatic fast open each kind of incandescent lamp, the fluorescent lamp, the buzzer, the automatic valve, the electric fan, the dryer and automatically washes the hands installment and so on pond, specially is suitable for the enterprise, Sensitive areas such as the hotel , market , storehouse , corridor of the family , corridor ,etc., or is used in automatic light , the illumination and warning system of the safe area. The customer of interest can buy the print to test .

Craft ,

- *CMOS of characteristic ,
- * count mould mix ,
- * have independent high input impedance operation amplifier
- * internal two-way ancient bronze mirror pieces of device can inhibit from , interfere with
- * set up the time timing of the delay effectively Device and blockade time timer
- * adopt 16 foot DIP encapsulate , in charge of foot picture

In charge of the foot picture



In charge of foot prove pin function ,

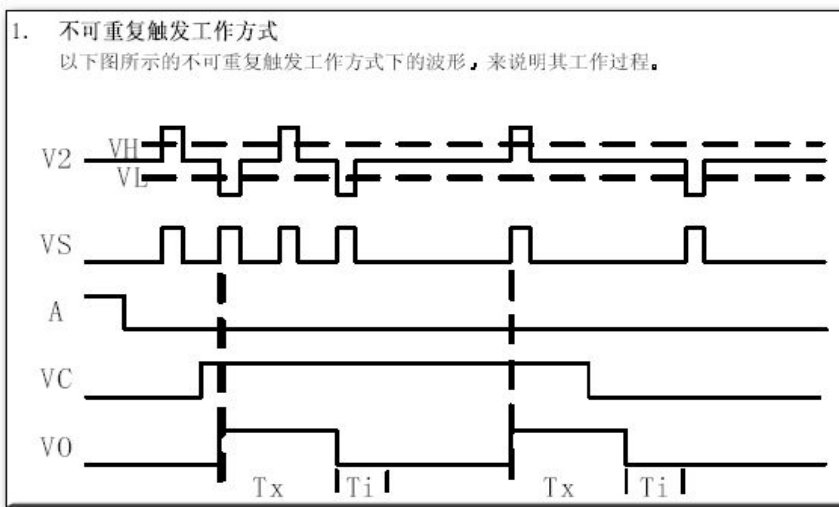
I/O of name , prove

- 1 A I can touch off and can't touch off , choose end repeatedly repeatedly. When A is " 1 " , allow to touch off repeatedly; On the contrary , can't touch off
- 2 V OO control signal output end repeatedly . Jump , become , along touch off , make Vo export , jump , become , regard from low level to high cable as to touch off effectively at ordinary times at VS. Have , jump become , at VS except exporting the time Tx of delay, Vo keep low level state.
- 3 R R1 --Export the regulation end
- 4 R C1 of Tx of time of delay --Export the regulation end
- 5 R C2 of Tx of time of delay --Touch off and blockade the time regulation end
- 6 R R2 of Ti --Touch off and blockade the time regulation end
- 7 V SS of Ti --Working power shoulder the end
- 8 V RFI reference voltage and reduce the input terminus. Connect VDD usually , can make timer restore to the throne
- 9 V CI touch off , forbid end when connecting " 0 ". Forbid touching off as VcVR
- 10 I B --The operation amplifier setovers the electric current and sets up the end

- 11 V DD --Working power carry
- 12 2 OUTO the second operation output end
- 13 2 IN-I the second operation amplifier of amplifier against input terminus
- 14 1 IN I the first operation amplifier of looks And looks input terminus against looks input terminus
- 16 1 OUTO the first operation the output ends of amplifier in amplifier in the
- 15 1 IN-I the first operation

Operation principle

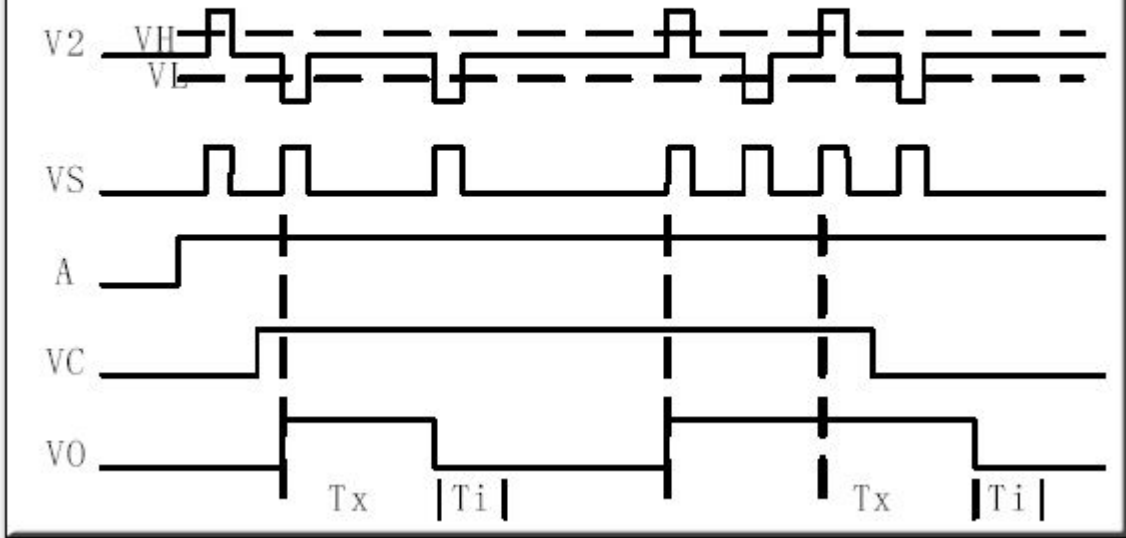
BISS0001 is by the operation amplifier , voltage comparator , state controller , delay time timer and blockade time timer ,etc. number mould that commit mix the special-purpose integrated circuit.



Whom following picture show can't touch off wave form of working way repeatedly, come , prove working course its. Can't touch off the wave form under the working way at first , according to the needs of reality repeatedly , utilize the operation amplifier OP1 to make up the signal preconditioning circuit of sensing , amplifies the signal. Then coupling give operation amplifier OP2, go on for the second again , enlarge, rebound direct current location at the same time for behind the VM ($\sim 0.5V_{DD}$), output signal V2 send composed of comparator COP1 and COP2 two-way ancient bronze mirror pieces of device to , examine , produce , touch off the signal Vs effectively. Because of $V_H \sim 0.7V_{DD}$, $V_L \sim 0.3V_{DD}$, so, acts as $V_{DD} = 5V$, can inhibit \pm noise of 1V from interfere with , improves systematic dependability effectively. COP3 is a condition comparator. When inputting the voltage V_{cVR} , COP3 exports for the high level, enters the cycle of delaying time. Connect as A end " 0 " electricity at ordinary times , change of V2 any neglect within Tx time , until Tx time is over, i.e. as it always goes, Can't touch off the working way repeatedly . When Tx time finishes , jump , go back low level under the Vo , start , blockade time timer enter and blockade cycle Ti at the same time. Within Ti time, change of V2 any can make Vo jump , turn effective state (high level) into, can inhibit load from switching over various interference produced in the course effectively .

2. 可重复触发工作方式

以下图所示的可重复触发工作方式下的波形，来说明其工作过程。



What the following pictures are shown can touch off the wave form under the working way repeatedly , to explain its working course. Can touch off the wave form under the working way in Vc repeatedly = " 0 " , A =During the " 0 " , signal Vs can't touch off Vo as the valid state . In Vc = " 1 " , A =When the " 1 " , Vs can touch off Vo as effective state , can impel Vo remain valid state within Tx cycle repeatedly. Within Tx time , jump , change at Vs take place, Vo jump , become case continue , lengthen one Tx cycle constantly from Vs then; If Vs keeps as " 1 " state, then the state that Vo has been remaining valid all the time ; If Vs keeps as " 0 " state, Vo resumes for the invalid state after Tx cycle, and, in blocking time Ti time too, changes of any Vs can't touch off Vo as the effective state .

Employ the line map

